UTILITY **PATENT APPLICATION** TRANSMITTAL (CAPOr new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 251383US2CONT

First Inventor or Application Identifier

Takashi YAMADA

Registration No.: | 50,607

Title SEMICONDUCTOR CHIP HAVING MULTIPLE FUNCTIONAL BLOCKS INTEGRATED IN A SINGLE CHIP AND METHOD FOR FABRICATING THE SAME

APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents			ADDRESS TO: Commissioner for Patents Mail Stop Patent Application Alexandria, Virginia 22313			834 U		
1.	Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)		ACCOMPANYING APPLICATION PARTS					
_			_	signment Record			11/0723	
2.	Specification	Total Sheets 41	,	plication Data St			•	
			9. □ 37 (whe	C.F.R. §3.73(b) en there is an assign	Statement ee)	□ Power Attorn		
3.	Drawing(s) (35 U.S.C. 113)	Total Sheets 19	10. 🗆 Eng	glish Translation	Document (ii	f applicable))	
				ormation Disclos atement (IDS)/P1		□ Copie: Citatio	s of IDS ons	
4.	Oath or Declaration	Total Pages 4		eliminary Amend				
a.	a. Newly executed (original or copy)			13. White Advance Serial No. Postcard				
b.	(for continuation/divisional w	,		rtified Copy of P preign priority is claim		ent(s)		
		NVENTOR(S) ached deleting inventor(s) named in see 37 C.F.R. §1.63(d)(2) and		plicant claims sn 37 CFR 1.27	nall entity stat	us.		
5. 🗆	' '	ate, large table or Computer	16. 🔳 Oth	her: Reque	est for Priority			
6. 🗆	Nucleotide and/or Amino Ad (if applicable, all necessary	<i>.</i>	i.					
a.	Computer Readable Fo	, ,						
b.	Specification or Sequence L i. □ CD-ROM or CD-R (2							
	ii. ☐ Paper	30p.30), 0.						
c.	☐ Statements verifying ide	entity of above copies						
17. If a	CONTINUING APPLICATIO	N, check appropriate box, and suppl	y the requisite in	nformation below:				
	Continuation	onal Continuation-	in-part (CIP)	of prior a	pplication no.	: 09/995,594	ŀ	
Prior	application information:	Examiner: DICKEY, T. L.		G	Froup Art Unit	: 2826		
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.								
18. CORRESPONDENCE ADDRESS								
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Signature: Kathushi & Fauly					Date: 4	21/04		



Docket No.

251383US2CONT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Takashi YAMADA, et al.

SERIAL NO:

New CONT Application

FILING DATE: Herewith

FOR:

SEMICONDUCTOR CHIP HAVING MULTIPLE FUNCTIONAL BLOCKS INTEGRATED IN A

SINGLE CHIP AND METHOD FOR FABRICATING THE SAME

FEE TRANSMITTAL

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS	
TOTAL CLAIMS	20 - 20 =	0	x \$18 =	\$0.00	
INDEPENDENT CLAIMS	2 - 3 =	0	x \$86 =	\$0.00	
☐ MULTIPLE DEPENDEN	+ \$290 =	\$0.00			
☐ LATE FILING OF DECL	+ \$130 =	\$0.00			
	\$770.00				
	\$770.00				
☐ REDUCTION BY 50% F	\$0.00				
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*;	The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to Deposit Account No. <u>15-0030</u> . A duplicate copy of this sheet is enclosed.						
ı		Respectfully Submitted,					
		OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.					
Dat	re: 4/21/04	Kathurin W. Paully Eckhard H. Kuesters					
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